

CLAIMS

What is claimed is:

- 1 ~~1.~~ A method comprising dynamically establishing ATM adaptation layer 2 (AAL2)
2 channel identifiers (CIDs) on a call-by-call basis using ATM standards-based call control
3 signaling protocols.
- 1 2. The method of claim 1 further comprising mapping the CIDs to a virtual
2 path/virtual channel (VP/VC) that forms part of a virtual user network interface (UNI) to
3 an ATM network.
- 1 ~~3.~~ An ATM node configured to dynamically establish ATM adaptation layer 2
2 (AAL2) channel identifiers (CIDs) on a call-by-call basis using ATM standards-based
3 call control signaling protocols.
- 1 4. The ATM node of claim 3 further configured to map each of the CIDs to a virtual
2 path/virtual channel (VP/VC) that forms part of a virtual user network interface (UNI) to
3 an ATM network.
- 1 5. A method comprising mapping ATM adaptation layer 2 (AAL2) channel
2 identifiers (CIDs) to a virtual path/virtual channel (VP/VC) within a standards-based
3 ATM call control protocol.
- 1 ~~6.~~ The method of claim 5 wherein the standards-based ATM call control protocol is
2 selected from the list comprising UNI 3.1/4.0 and Q.2931.
- 1 7. The method of claim 5 wherein the mapping is performed at a network edge
2 device communicatively coupled to customer premises equipment.

1 8. The method of claim 7 wherein the network edge device is communicatively
2 coupled to the customer premises equipment over time division multiplexed
3 communication channels.

1 9. The method of claim 8 further comprising mutliplexing the time division
2 multiplexed communication channels to one or more AAL2 VPs/VCs.

1 10. The method of claim 9 further comprising mapping the one or more AAL2
2 VPs/VCs to the CIDs prior to mapping the CIDs to the VP/VC.

1 11. Computer-readable instructions, which when implemented by a processor, cause
2 the processor to map ATM adaptation layer 2 (AAL2) channel identifiers (CIDs) to a
3 virtual path/virtual channel (VP/VC) within a standard-based ATM call control protocol.

1 12. A computer-readable medium embodying the computer-readable instructions of
2 claim 11.

1 13. The computer-readable instructions of claim 11 further comprising additional
2 instructions, which when implemented by the processor, cause the processor to multiplex
3 one or more time division multiplexed communication channels to one or more AAL2
4 VPs/VCs prior to mapping the AAL2 CIDs to the VP/VC.

1 14. The computer-readable instructions of claim 13 further comprising yet more
2 instructions, which when executed by the processor, cause the processor to map the one
3 or more AAL2 VPs/VCs to the CIDs prior to mapping the CIDs to the VP/VC.